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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hiroyoshi TOMITA

Group Art Unit: 2818

Application No.: 09/587,296

Examiner: C. Yoha

Filed: June 5, 2000

Attorney Dkt. No.: 108397-00067

For: SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD OF CONTROLLING THE  
SAME, AND VARIABLE DELAY CIRCUIT

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Date: February 21, 2002

Sir:

Prior to initial examination of the application, please amend the above-identified  
application as follows:

**IN THE CLAIMS:**

Please cancel claims 1-19 without prejudice or disclaimer.